



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rajiv Jain and Richard J. Wong
Assignee: QuickLogic Corporation
Title: Method of Programming an Antifuse
Serial No.: 09/887,834 Filing Date: June 22, 2001
Examiner: T. Cunningham Group Art Unit: 2816
Docket No.: M-9351 US

#3/a
7/1/02
Smith

San Jose, California
June 25, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

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RESPONSE TO OFFICE ACTION

Dear Sir:

This is in response to the Office Action with a mailing date of March 27, 2002. Please amend the above-referenced application as follows.

IN THE CLAIMS

Claims 1, 4, 7, 8, 9, 18, 19, 21, 22, and 24 have been amended. The following is a clean set of the entire set of claims. In accordance with 37 C.F.R. § 1.121 (c)(1)(ii), a marked up version of the amended claims is included in an Appendix.

07/02/2002 JADD01 00000023 192386 09887834
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1. (Amended) A method of programming an antifuse, said antifuse comprising a material that is substantially non-conductive when said antifuse is unprogrammed, said material being disposed between and in electrical contact with a first conductive element and a second conductive element, said method comprising: